



Docket No.: 50100-786

PATENT

8/A (initials)
6/5/02
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : **RESPONSE UNDER 37 CFR 1.116**
John CHIANG, et al. : **EXPEDITED PROCEDURE**
Serial No.: 09/304,964 : Group Art Unit: 2664
Filed: May 05, 1999 : Examiner: Kwang B. Yao
For: DYNAMIC TIME SLOT ALLOCATION IN INTERNAL RULES CHECKER
SCHEDULER

AMENDMENT UNDER 37 CFR 1.116

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JUN 0 4 2002

Technology Center 2100

Box AF
Commissioner for Patents
Washington, DC 20231

Sir:

In response to the Office Action mailed April 4, 2002, please amend the above-identified application as follows:

IN THE CLAIMS

Claims 2, 8 and 13 now read as follows:

2. (Amended) A multiport data communication system for switching data packets between ports, the data communication system comprising:
- a plurality of receive ports for receiving data packets,
 - a decision making engine responsive to the received data packets for controlling transmission of the received data packets to at least one selected transmit port,
 - the decision making engine including: